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April 2, 2004

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
EA	6,307,252	10/2001	KNOEDL			

FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
EA 10-256250	09/1998	JAPAN			PARTIAL

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

EA	"On-Chip Spiral Inductors with Patterned Ground Shields for Si-Based RF IC's" [C. Patrick Yue et al., IEEE Journal of Solid-State Circuits, vol. 33, No.5, pp. 743-752, May 1998]
EA	"Deep Trench Guard Technology to Suppress Coupling between Inductors in Silicon RF ICs" [Cheon Soo Kimm et al., 2001 IEEE]
EA	"Future EMC Trends in PC Board Design" [Bruce Gabrielson, EMC EXPO 1996, June 16 to 19, 1986]

*Examiner

EA

Date Considered

7-15-05

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.